



# STx25NM50N

N-channel 500 V, 0.11  $\Omega$ , 22 A MDmesh™ II Power MOSFET  
TO-220, TO-220FP, I<sup>2</sup>PAK, D<sup>2</sup>PAK, TO-247

## Features

| Type         | V <sub>DSS</sub><br>(@T <sub>jmax</sub> ) | R <sub>DS(on)</sub><br>max | I <sub>D</sub>      |
|--------------|-------------------------------------------|----------------------------|---------------------|
| STB25NM50N   | 550 V                                     | < 0.140 $\Omega$           | 22 A                |
| STB25NM50N-1 | 550 V                                     | < 0.140 $\Omega$           | 22 A                |
| STF25NM50N   | 550 V                                     | < 0.140 $\Omega$           | 22 A <sup>(1)</sup> |
| STP25NM50N   | 550 V                                     | < 0.140 $\Omega$           | 22 A                |
| STW25NM50N   | 550 V                                     | < 0.140 $\Omega$           | 22 A                |

1. Limited only by maximum temperature allowed

- 100% avalanche tested
- Low input capacitance and gate charge
- Low gate input resistance

## Application

- Switching applications

## Description

This series of devices is realized with the second generation of MDmesh™ Technology. This revolutionary MOSFET associates a new vertical structure to the Company's strip layout to yield one of the world's lowest on-resistance and gate charge. It is therefore suitable for the most demanding high efficiency converters.

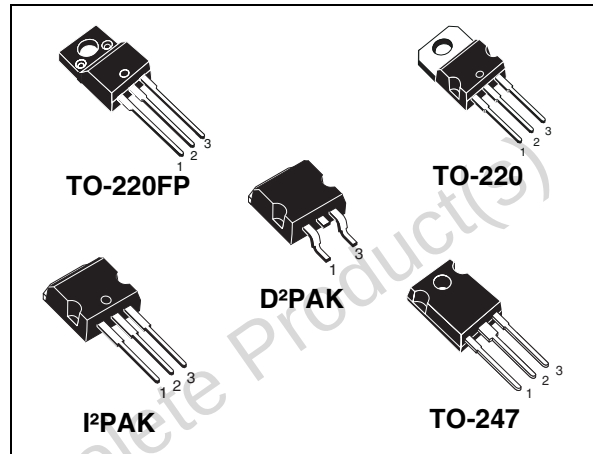


Figure 1. Internal schematic diagram

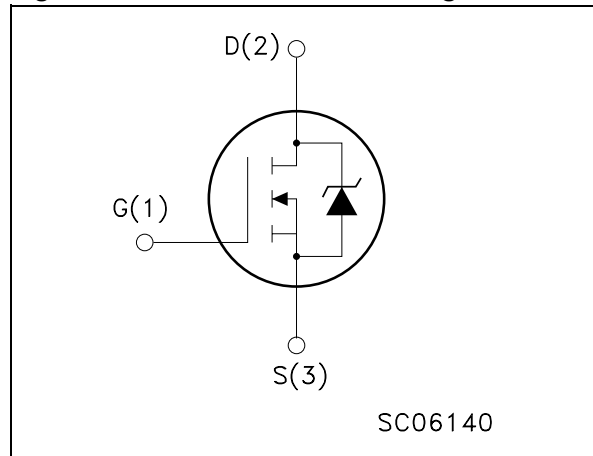


Table 1. Device summary

| Order codes  | Marking  | Package            | Packaging     |
|--------------|----------|--------------------|---------------|
| STB25NM50N   | B25NM50N | D <sup>2</sup> PAK | Tape and reel |
| STB25NM50N-1 | B25NM50N | I <sup>2</sup> PAK | Tube          |
| STF25NM50N   | F25NM50N | TO-220FP           | Tube          |
| STP25NM50N   | P25NM50N | TO-220             | Tube          |
| STW25NM50N   | W25NM50N | TO-247             | Tube          |

Contents

1      **Electrical ratings** ..... 3

2      **Electrical characteristics** ..... 4

      2.1    Electrical characteristics (curves) ..... 6

3      **Test circuit** ..... 9

4      **Package mechanical data** ..... 10

5      **Packaging mechanical data** ..... 16

6      **Revision history** ..... 18

# 1 Electrical ratings

**Table 2. Absolute maximum ratings**

| Symbol                         | Parameter                                                                                                    | Value                                                    |                   | Unit |
|--------------------------------|--------------------------------------------------------------------------------------------------------------|----------------------------------------------------------|-------------------|------|
|                                |                                                                                                              | TO-220, I <sup>2</sup> PAK<br>D <sup>2</sup> PAK, TO-247 | TO-220FP          |      |
| V <sub>DS</sub>                | Drain-source voltage (V <sub>GS</sub> = 0)                                                                   | 500                                                      |                   | V    |
| V <sub>GS</sub>                | Gate- source voltage                                                                                         | ±25                                                      |                   | V    |
| I <sub>D</sub>                 | Drain current (continuous) at T <sub>C</sub> = 25 °C                                                         | 22                                                       | 22 <sup>(1)</sup> | A    |
| I <sub>D</sub>                 | Drain current (continuous) at T <sub>C</sub> = 100 °C                                                        | 14                                                       | 14 <sup>(1)</sup> | A    |
| I <sub>DM</sub> <sup>(2)</sup> | Drain current (pulsed)                                                                                       | 88                                                       | 88 <sup>(1)</sup> | A    |
| P <sub>TOT</sub>               | Total dissipation at T <sub>C</sub> = 25 °C                                                                  | 160                                                      | 40                | W    |
|                                | Derating factor                                                                                              | 1.28                                                     | 0.32              | W/°C |
| V <sub>ISO</sub>               | Insulation withstand voltage (RMS) from all three leads to external heat sink (t=1 s; T <sub>C</sub> =25 °C) | --                                                       | 2500              | V    |
| dv/dt <sup>(3)</sup>           | Peak diode recovery voltage slope                                                                            | 15                                                       |                   | V/ns |
| T <sub>stg</sub>               | Storage temperature                                                                                          | -55 to 150                                               |                   | °C   |
| T <sub>j</sub>                 | Max. operating junction temperature                                                                          | 150                                                      |                   | °C   |

- Limited only by maximum temperature allowed
- Pulse width limited by safe operating area
- I<sub>SD</sub> ≤ 22 A, di/dt ≤ 400 A/μs, V<sub>DD</sub> = 80% V<sub>(BR)DSS</sub>

**Table 3. Thermal data**

| Symbol                | Parameter                                      | Value                                                    |          | Unit |
|-----------------------|------------------------------------------------|----------------------------------------------------------|----------|------|
|                       |                                                | TO-220, I <sup>2</sup> PAK<br>D <sup>2</sup> PAK, TO-247 | TO-220FP |      |
| R <sub>thj-case</sub> | Thermal resistance junction-case max           | 0.78                                                     | 3.1      | °C/W |
| R <sub>thj-amb</sub>  | Thermal resistance junction-ambient max        | 62.5                                                     |          | °C/W |
| T <sub>l</sub>        | Maximum lead temperature for soldering purpose | 300                                                      |          | °C   |

**Table 4. Avalanche characteristics**

| Symbol          | Parameter                                                                                                                  | Value | Unit |
|-----------------|----------------------------------------------------------------------------------------------------------------------------|-------|------|
| I <sub>AR</sub> | Avalanche current, repetitive or not-repetitive (pulse width limited by T <sub>j</sub> Max)                                | 10    | A    |
| E <sub>AS</sub> | Single pulse avalanche energy (starting T <sub>j</sub> = 25 °C, I <sub>D</sub> = I <sub>AR</sub> , V <sub>DD</sub> = 50 V) | 350   | mJ   |

## 2 Electrical characteristics

( $T_{CASE}=25^{\circ}\text{C}$  unless otherwise specified)

**Table 5. On/off states**

| Symbol        | Parameter                                        | Test conditions                                                              | Min. | Typ.  | Max.    | Unit                           |
|---------------|--------------------------------------------------|------------------------------------------------------------------------------|------|-------|---------|--------------------------------|
| $V_{(BR)DSS}$ | Drain-source breakdown voltage                   | $I_D = 1 \text{ mA}$ , $V_{GS} = 0$                                          | 500  |       |         | V                              |
| $dv/dt^{(1)}$ | Drain source voltage slope                       | $V_{DD} = 400 \text{ V}$ , $I_D = 25 \text{ A}$ ,<br>$V_{GS} = 10 \text{ V}$ |      | 44    |         | V/ns                           |
| $I_{DSS}$     | Zero gate voltage drain current ( $V_{GS} = 0$ ) | $V_{DS} = \text{Max rating}$<br>$V_{DS} = \text{Max rating}$ , @125 °C       |      |       | 1<br>10 | $\mu\text{A}$<br>$\mu\text{A}$ |
| $I_{GSS}$     | Gate-body leakage current ( $V_{DS} = 0$ )       | $V_{GS} = \pm 20 \text{ V}$                                                  |      |       | 100     | nA                             |
| $V_{GS(th)}$  | Gate threshold voltage                           | $V_{DS} = V_{GS}$ , $I_D = 250 \mu\text{A}$                                  | 2    | 3     | 4       | V                              |
| $R_{DS(on)}$  | Static drain-source on resistance                | $V_{GS} = 10 \text{ V}$ , $I_D = 11 \text{ A}$                               |      | 0.110 | 0.140   | $\Omega$                       |

1. Characteristic value at turn off on inductive load

**Table 6. Dynamic**

| Symbol                              | Parameter                                                               | Test conditions                                                                                   | Min. | Typ.              | Max. | Unit           |
|-------------------------------------|-------------------------------------------------------------------------|---------------------------------------------------------------------------------------------------|------|-------------------|------|----------------|
| $g_{fs}^{(1)}$                      | Forward transconductance                                                | $V_{DS} = 15 \text{ V}$ , $I_D = 11 \text{ A}$                                                    |      | 19                |      | S              |
| $C_{iss}$<br>$C_{oss}$<br>$C_{rss}$ | Input capacitance<br>Output capacitance<br>Reverse transfer capacitance | $V_{DS} = 25 \text{ V}$ , $f = 1 \text{ MHz}$ ,<br>$V_{GS} = 0$                                   |      | 2565<br>511<br>77 |      | pF<br>pF<br>pF |
| $C_{oss \text{ eq.}}^{(2)}$         | Equivalent output capacitance                                           | $V_{GS} = 0$ , $V_{DS} = 0 \text{ to } 400 \text{ V}$                                             |      | 315               |      | pF             |
| $Q_g$<br>$Q_{gs}$<br>$Q_{gd}$       | Total gate charge<br>Gate-source charge<br>Gate-drain charge            | $V_{DD} = 400 \text{ V}$ , $I_D = 22 \text{ A}$ ,<br>$V_{GS} = 10 \text{ V}$ ,<br>(see Figure 19) |      | 84<br>11<br>35    |      | nC<br>nC<br>nC |
| $R_g$                               | Gate input resistance                                                   | $f = 1 \text{ MHz}$ Gate DC Bias=0<br>Test signal level=20 mV<br>open drain                       |      | 1.6               |      | $\Omega$       |

1. Pulsed: Pulse duration = 300  $\mu\text{s}$ , duty cycle 1.5%

2.  $C_{oss \text{ eq.}}$  is defined as a constant equivalent capacitance giving the same charging time as  $C_{oss}$  when  $V_{DS}$  increases from 0 to 80%  $V_{DS}$

**Table 7. Switching times**

| Symbol       | Parameter           | Test conditions                                                                                                | Min. | Typ. | Max. | Unit |
|--------------|---------------------|----------------------------------------------------------------------------------------------------------------|------|------|------|------|
| $t_{d(on)}$  | Turn-on delay time  | $V_{DD} = 250\text{ V}$ , $I_D = 11\text{ A}$<br>$R_G = 4.7\ \Omega$ $V_{GS} = 10\text{ V}$<br>(see Figure 18) |      | 23   |      | ns   |
| $t_r$        | Rise time           |                                                                                                                |      | 23   |      | ns   |
| $t_{d(off)}$ | Turn-off delay time |                                                                                                                |      | 75   |      | ns   |
| $t_f$        | Fall time           |                                                                                                                |      | 22   |      | ns   |

**Table 8. Source drain diode**

| Symbol          | Parameter                     | Test conditions                                                                                                                               | Min | Typ. | Max | Unit          |
|-----------------|-------------------------------|-----------------------------------------------------------------------------------------------------------------------------------------------|-----|------|-----|---------------|
| $I_{SD}$        | Source-drain current          |                                                                                                                                               |     |      | 22  | A             |
| $I_{SDM}^{(1)}$ | Source-drain current (pulsed) |                                                                                                                                               |     |      | 88  | A             |
| $V_{SD}^{(2)}$  | Forward on voltage            | $I_{SD} = 22\text{ A}$ , $V_{GS} = 0$                                                                                                         |     |      | 1.3 | V             |
| $t_{rr}$        | Reverse recovery time         | $I_{SD} = 22\text{ A}$ , $di/dt = 100\text{ A}/\mu\text{s}$<br>$V_{DD} = 100\text{ V}$<br>(see Figure 23)                                     |     | 460  |     | ns            |
| $Q_{rr}$        | Reverse recovery charge       |                                                                                                                                               |     | 6.9  |     | $\mu\text{C}$ |
| $I_{RRM}$       | Reverse recovery current      |                                                                                                                                               |     | 30   |     | A             |
| $t_{rr}$        | Reverse recovery time         | $I_{SD} = 22\text{ A}$ , $di/dt = 100\text{ A}/\mu\text{s}$<br>$V_{DD} = 100\text{ V}$ , $T_J = 150\text{ }^\circ\text{C}$<br>(see Figure 23) |     | 532  |     | ns            |
| $Q_{rr}$        | Reverse recovery charge       |                                                                                                                                               |     | 8.25 |     | $\mu\text{C}$ |
| $I_{RRM}$       | Reverse recovery current      |                                                                                                                                               |     | 31   |     | A             |

1. Pulse width limited by safe operating area

2. Pulsed: Pulse duration = 300  $\mu\text{s}$ , duty cycle 1.5%

## 2.1 Electrical characteristics (curves)

Figure 2. Safe operating area for TO-220 / D<sup>2</sup>PAK / I<sup>2</sup>PAK

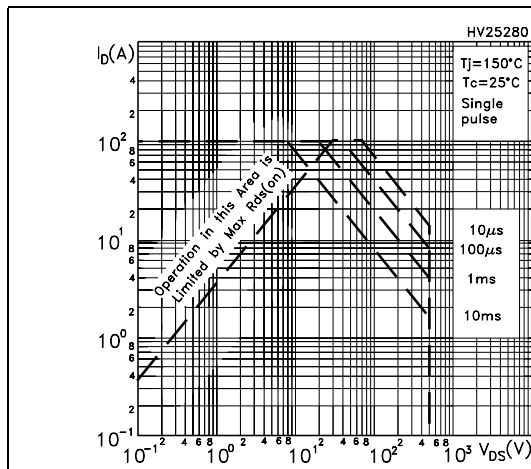


Figure 3. Thermal impedance for TO-220 / D<sup>2</sup>PAK / I<sup>2</sup>PAK

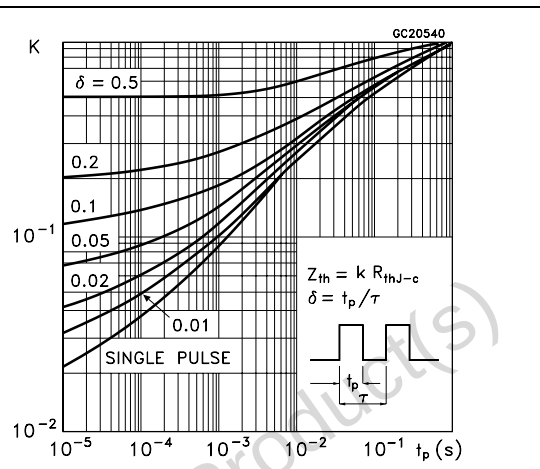


Figure 4. Safe operating area for TO-220FP

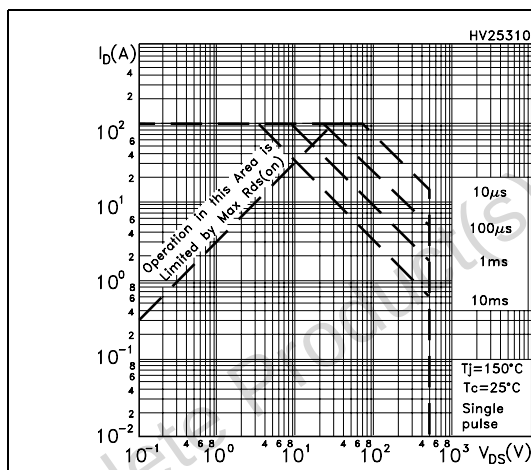


Figure 5. Thermal impedance for TO-220FP

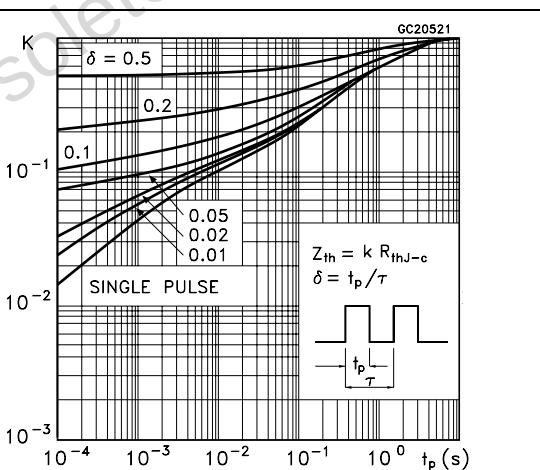


Figure 6. Safe operating area for TO-247

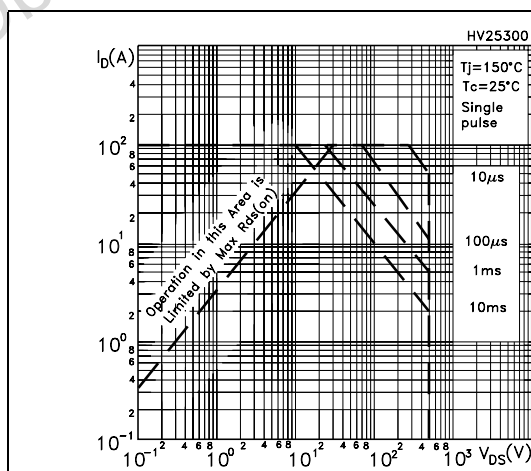


Figure 7. Thermal impedance for TO-247

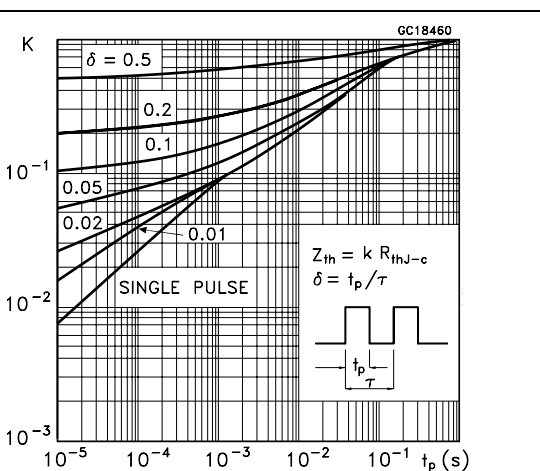


Figure 8. Output characteristics

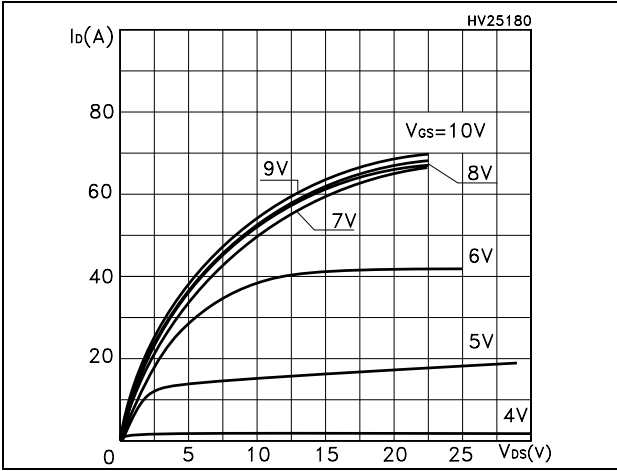


Figure 9. Transfer characteristics

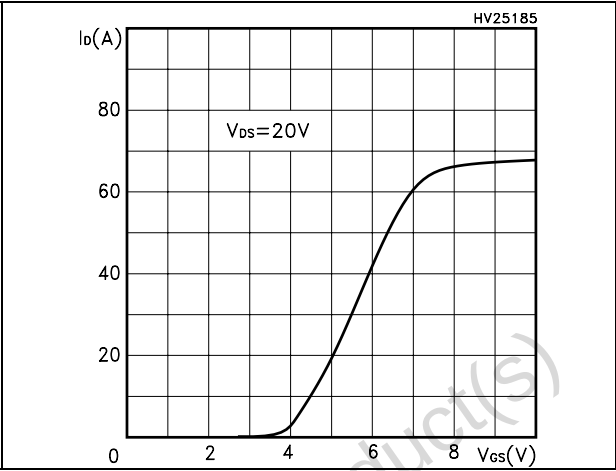


Figure 10. Transconductance

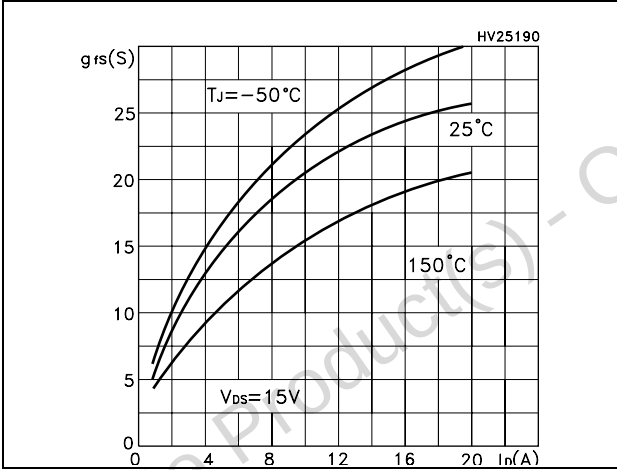


Figure 11. Static drain-source on resistance

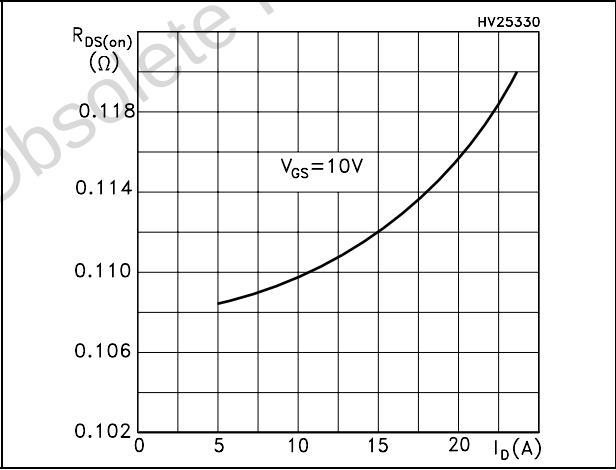


Figure 12. Gate charge vs gate-source voltage

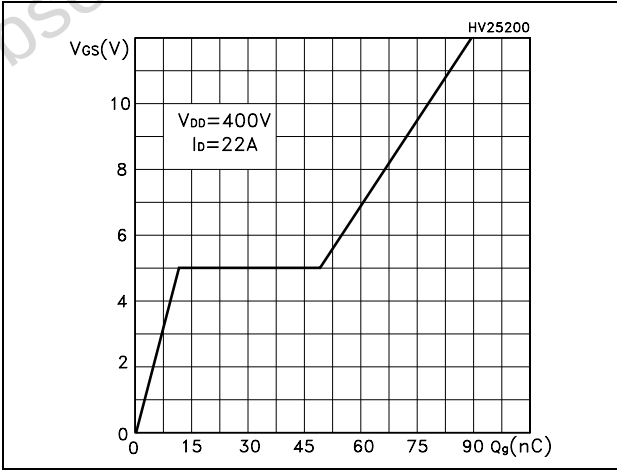


Figure 13. Capacitance variations

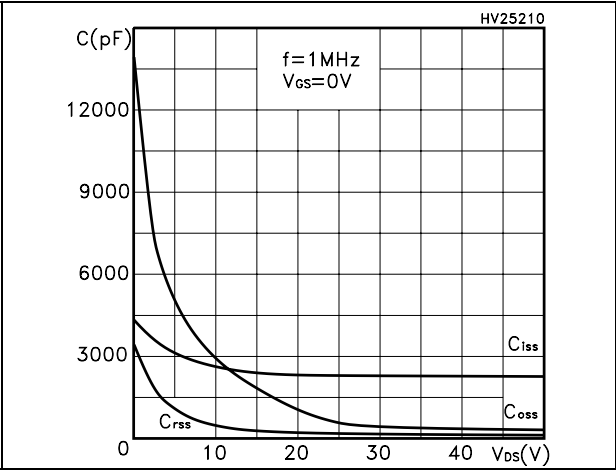


Figure 14. Normalized gate threshold voltage vs temperature

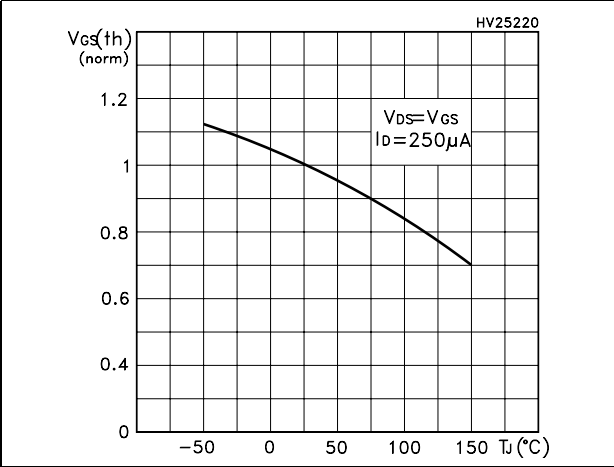


Figure 15. Normalized on resistance vs temperature

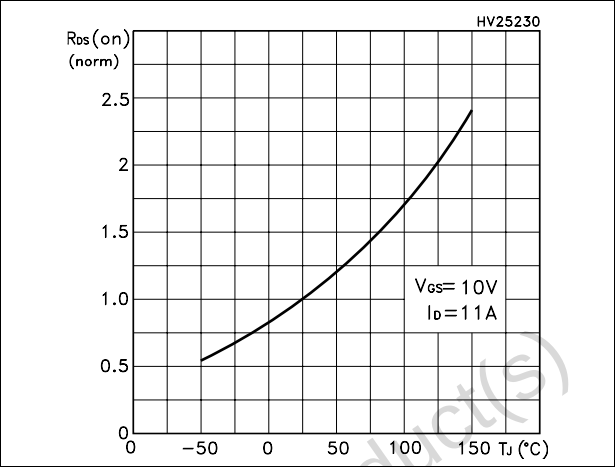


Figure 16. Source-drain diode forward characteristics

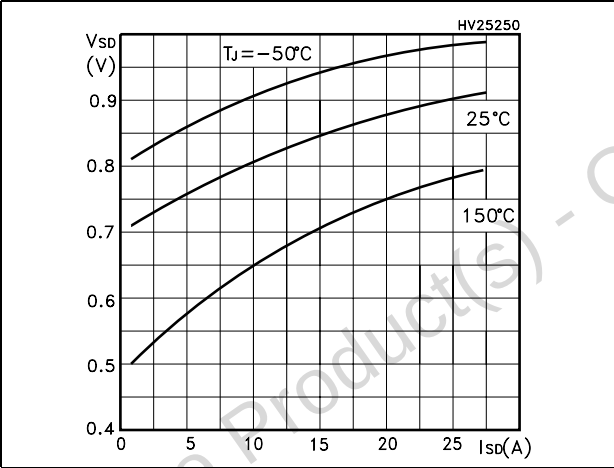
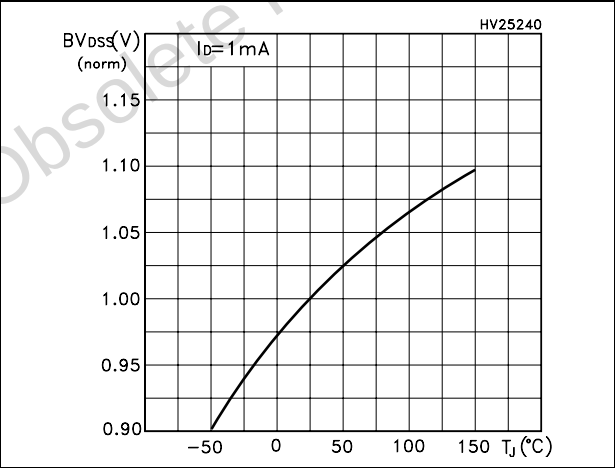


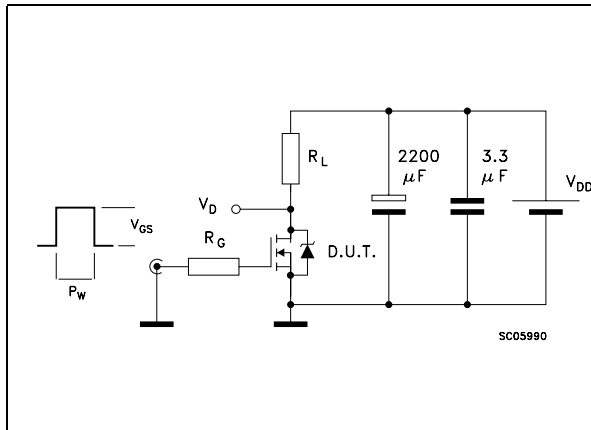
Figure 17. Normalized  $B_{V_{DS}}$  vs temperature



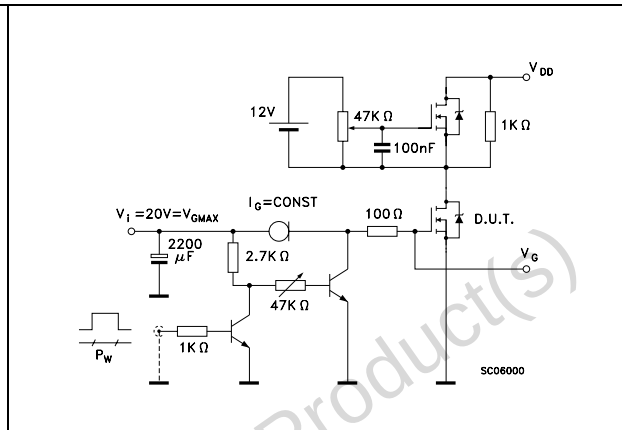


### 3 Test circuit

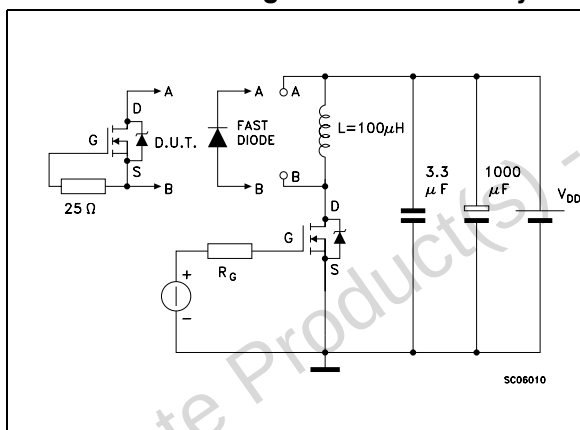
**Figure 18. Switching times test circuit for resistive load**



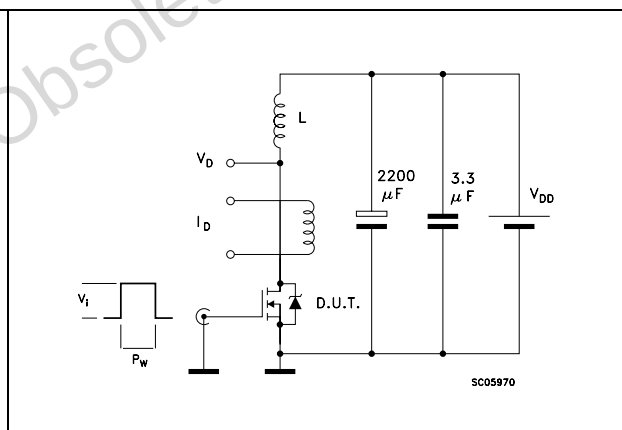
**Figure 19. Gate charge test circuit**



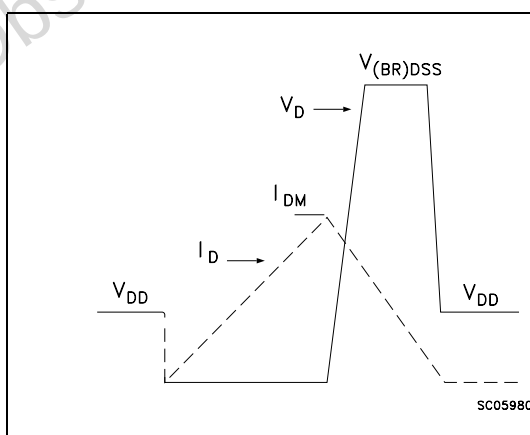
**Figure 20. Test circuit for inductive load switching and diode recovery times**



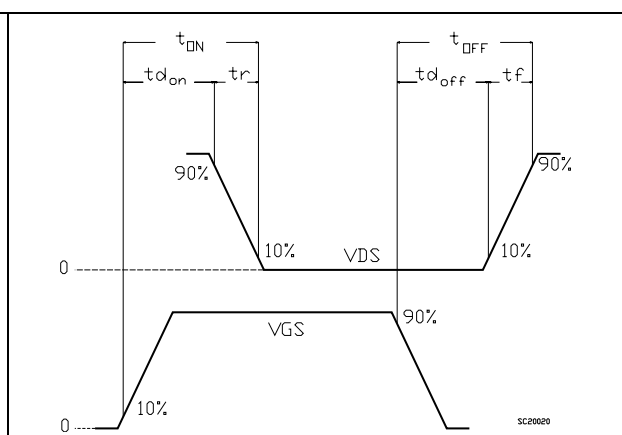
**Figure 21. Unclamped Inductive load test circuit**



**Figure 22. Unclamped inductive waveform**



**Figure 23. Switching time waveform**



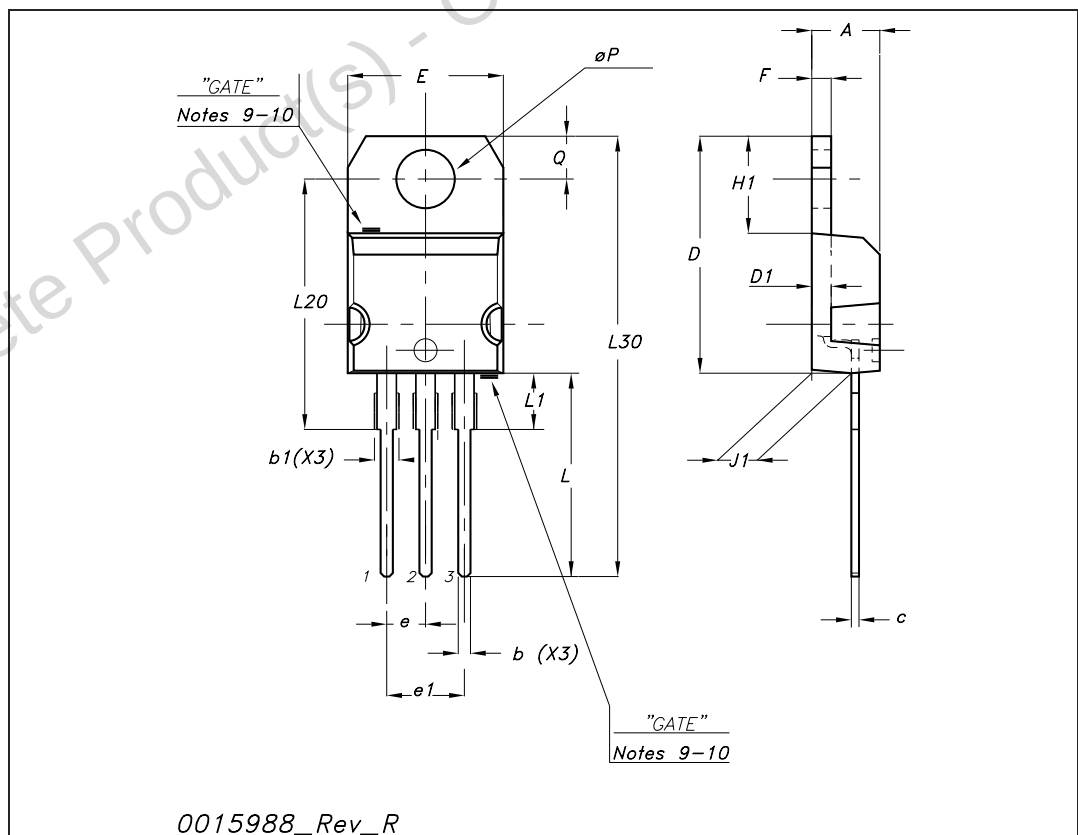
## 4 Package mechanical data

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: [www.st.com](http://www.st.com)

Obsolete Product(s) - Obsolete Product(s)

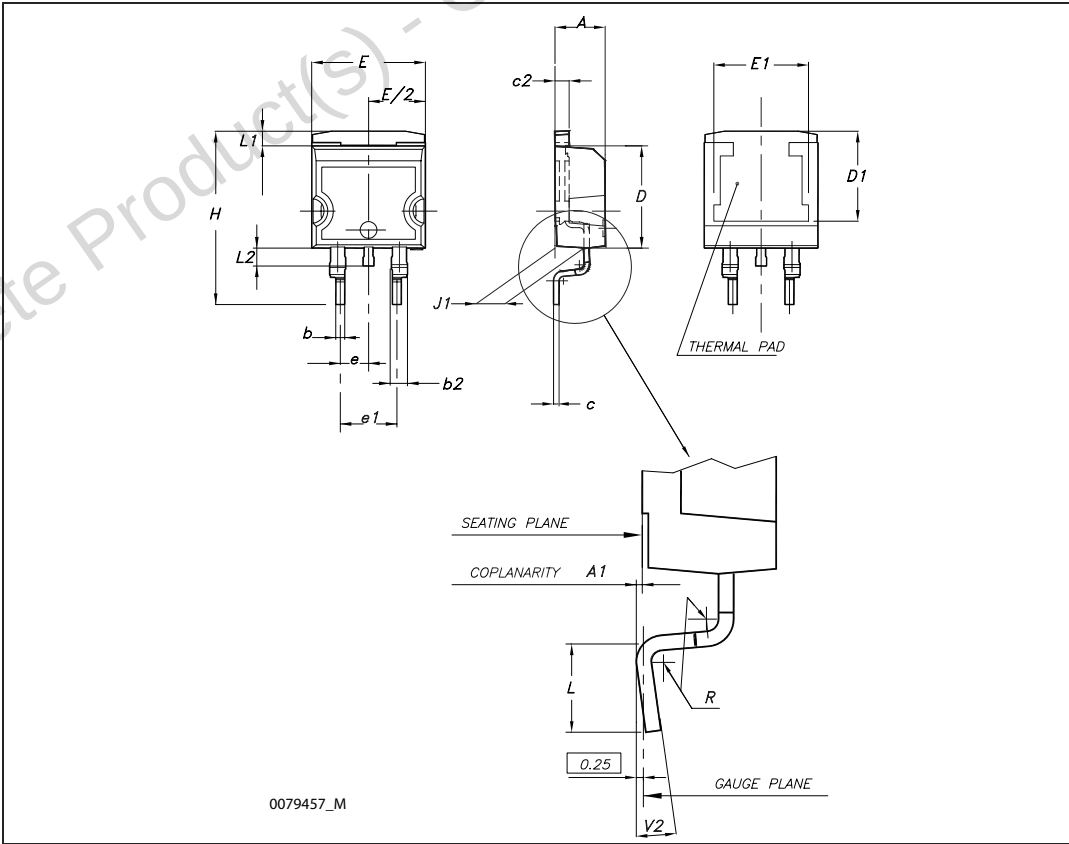
## TO-220 mechanical data

| Dim | mm    |       |       | inch  |       |       |
|-----|-------|-------|-------|-------|-------|-------|
|     | Min   | Typ   | Max   | Min   | Typ   | Max   |
| A   | 4.40  |       | 4.60  | 0.173 |       | 0.181 |
| b   | 0.61  |       | 0.88  | 0.024 |       | 0.034 |
| b1  | 1.14  |       | 1.70  | 0.044 |       | 0.066 |
| c   | 0.48  |       | 0.70  | 0.019 |       | 0.027 |
| D   | 15.25 |       | 15.75 | 0.6   |       | 0.62  |
| D1  |       | 1.27  |       |       | 0.050 |       |
| E   | 10    |       | 10.40 | 0.393 |       | 0.409 |
| e   | 2.40  |       | 2.70  | 0.094 |       | 0.106 |
| e1  | 4.95  |       | 5.15  | 0.194 |       | 0.202 |
| F   | 1.23  |       | 1.32  | 0.048 |       | 0.051 |
| H1  | 6.20  |       | 6.60  | 0.244 |       | 0.256 |
| J1  | 2.40  |       | 2.72  | 0.094 |       | 0.107 |
| L   | 13    |       | 14    | 0.511 |       | 0.551 |
| L1  | 3.50  |       | 3.93  | 0.137 |       | 0.154 |
| L20 |       | 16.40 |       |       | 0.645 |       |
| L30 |       | 28.90 |       |       | 1.137 |       |
| ØP  | 3.75  |       | 3.85  | 0.147 |       | 0.151 |
| Q   | 2.65  |       | 2.95  | 0.104 |       | 0.116 |



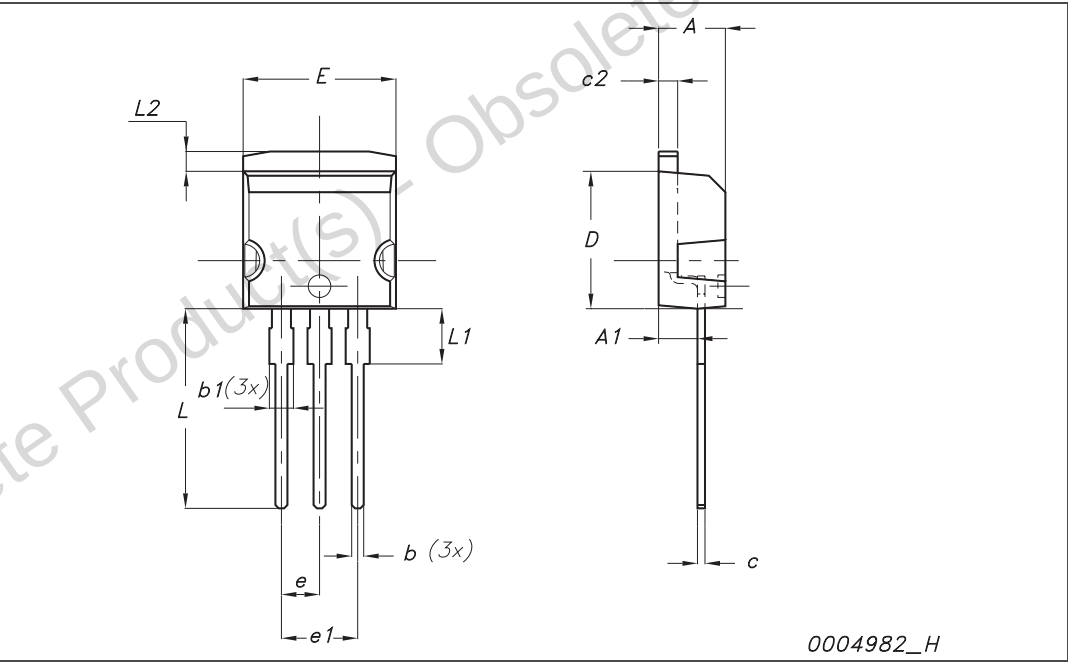
D<sup>2</sup>PAK (TO-263) mechanical data

| Dim | mm   |      |       | inch  |       |       |
|-----|------|------|-------|-------|-------|-------|
|     | Min  | Typ  | Max   | Min   | Typ   | Max   |
| A   | 4.40 |      | 4.60  | 0.173 |       | 0.181 |
| A1  | 0.03 |      | 0.23  | 0.001 |       | 0.009 |
| b   | 0.70 |      | 0.93  | 0.027 |       | 0.037 |
| b2  | 1.14 |      | 1.70  | 0.045 |       | 0.067 |
| c   | 0.45 |      | 0.60  | 0.017 |       | 0.024 |
| c2  | 1.23 |      | 1.36  | 0.048 |       | 0.053 |
| D   | 8.95 |      | 9.35  | 0.352 |       | 0.368 |
| D1  | 7.50 |      |       | 0.295 |       |       |
| E   | 10   |      | 10.40 | 0.394 |       | 0.409 |
| E1  | 8.50 |      |       | 0.334 |       |       |
| e   |      | 2.54 |       |       | 0.1   |       |
| e1  | 4.88 |      | 5.28  | 0.192 |       | 0.208 |
| H   | 15   |      | 15.85 | 0.590 |       | 0.624 |
| J1  | 2.49 |      | 2.69  | 0.099 |       | 0.106 |
| L   | 2.29 |      | 2.79  | 0.090 |       | 0.110 |
| L1  | 1.27 |      | 1.40  | 0.05  |       | 0.055 |
| L2  | 1.30 |      | 1.75  | 0.051 |       | 0.069 |
| R   |      | 0.4  |       |       | 0.016 |       |
| V2  | 0°   |      | 8°    | 0°    |       | 8°    |



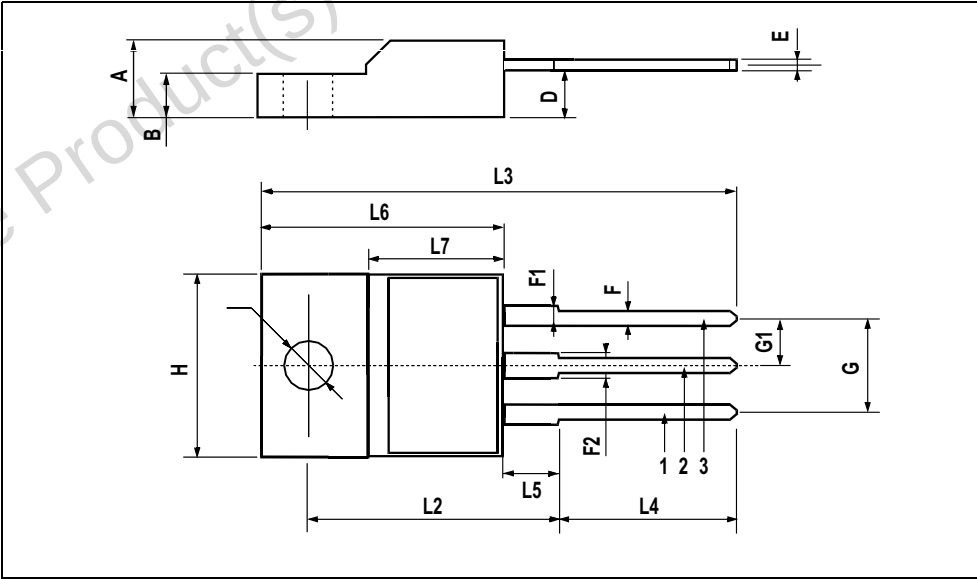
I<sup>2</sup>PAK (TO-262) mechanical data

| Dim | mm   |     |       | inch  |     |       |
|-----|------|-----|-------|-------|-----|-------|
|     | Min  | Typ | Max   | Min   | Typ | Max   |
| A   | 4.40 |     | 4.60  | 0.173 |     | 0.181 |
| A1  | 2.40 |     | 2.72  | 0.094 |     | 0.107 |
| b   | 0.61 |     | 0.88  | 0.024 |     | 0.034 |
| b1  | 1.14 |     | 1.70  | 0.044 |     | 0.066 |
| c   | 0.49 |     | 0.70  | 0.019 |     | 0.027 |
| c2  | 1.23 |     | 1.32  | 0.048 |     | 0.052 |
| D   | 8.95 |     | 9.35  | 0.352 |     | 0.368 |
| e   | 2.40 |     | 2.70  | 0.094 |     | 0.106 |
| e1  | 4.95 |     | 5.15  | 0.194 |     | 0.202 |
| E   | 10   |     | 10.40 | 0.393 |     | 0.410 |
| L   | 13   |     | 14    | 0.511 |     | 0.551 |
| L1  | 3.50 |     | 3.93  | 0.137 |     | 0.154 |
| L2  | 1.27 |     | 1.40  | 0.050 |     | 0.055 |



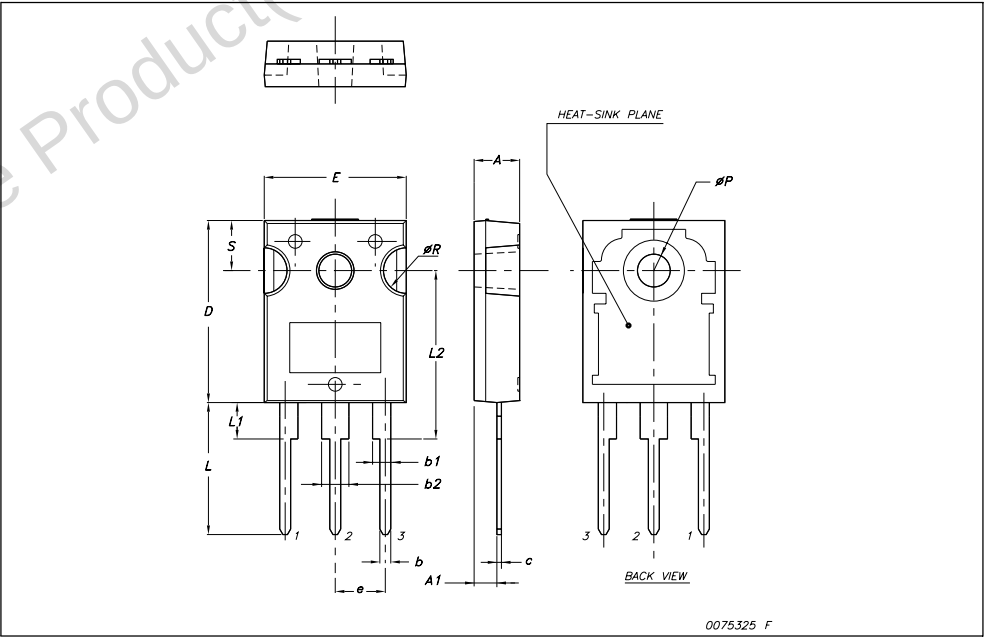
TO-220FP MECHANICAL DATA

| DIM. | mm.  |      |      | inch  |       |       |
|------|------|------|------|-------|-------|-------|
|      | MIN. | TYP. | MAX. | MIN.  | TYP.  | MAX.  |
| A    | 4.4  |      | 4.6  | 0.173 |       | 0.181 |
| B    | 2.5  |      | 2.7  | 0.098 |       | 0.106 |
| D    | 2.5  |      | 2.75 | 0.098 |       | 0.108 |
| E    | 0.45 |      | 0.7  | 0.017 |       | 0.027 |
| F    | 0.75 |      | 1    | 0.030 |       | 0.039 |
| F1   | 1.15 |      | 1.7  | 0.045 |       | 0.067 |
| F2   | 1.15 |      | 1.7  | 0.045 |       | 0.067 |
| G    | 4.95 |      | 5.2  | 0.195 |       | 0.204 |
| G1   | 2.4  |      | 2.7  | 0.094 |       | 0.106 |
| H    | 10   |      | 10.4 | 0.393 |       | 0.409 |
| L2   |      | 16   |      |       | 0.630 |       |
| L3   | 28.6 |      | 30.6 | 1.126 |       | 1.204 |
| L4   | 9.8  |      | 10.6 | .0385 |       | 0.417 |
| L5   | 2.9  |      | 3.6  | 0.114 |       | 0.141 |
| L6   | 15.9 |      | 16.4 | 0.626 |       | 0.645 |
| L7   | 9    |      | 9.3  | 0.354 |       | 0.366 |
| Ø    | 3    |      | 3.2  | 0.118 |       | 0.126 |



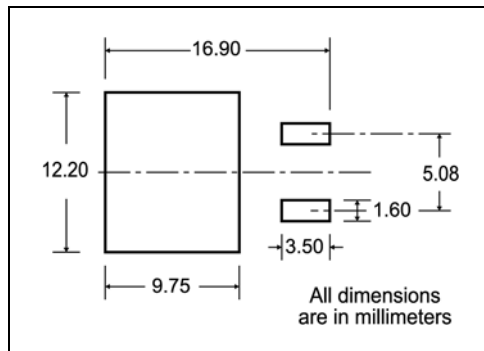
TO-247 Mechanical data

| Dim. | mm.   |       |       |
|------|-------|-------|-------|
|      | Min.  | Typ   | Max.  |
| A    | 4.85  |       | 5.15  |
| A1   | 2.20  |       | 2.60  |
| b    | 1.0   |       | 1.40  |
| b1   | 2.0   |       | 2.40  |
| b2   | 3.0   |       | 3.40  |
| c    | 0.40  |       | 0.80  |
| D    | 19.85 |       | 20.15 |
| E    | 15.45 |       | 15.75 |
| e    |       | 5.45  |       |
| L    | 14.20 |       | 14.80 |
| L1   | 3.70  |       | 4.30  |
| L2   |       | 18.50 |       |
| øP   | 3.55  |       | 3.65  |
| øR   | 4.50  |       | 5.50  |
| S    |       | 5.50  |       |

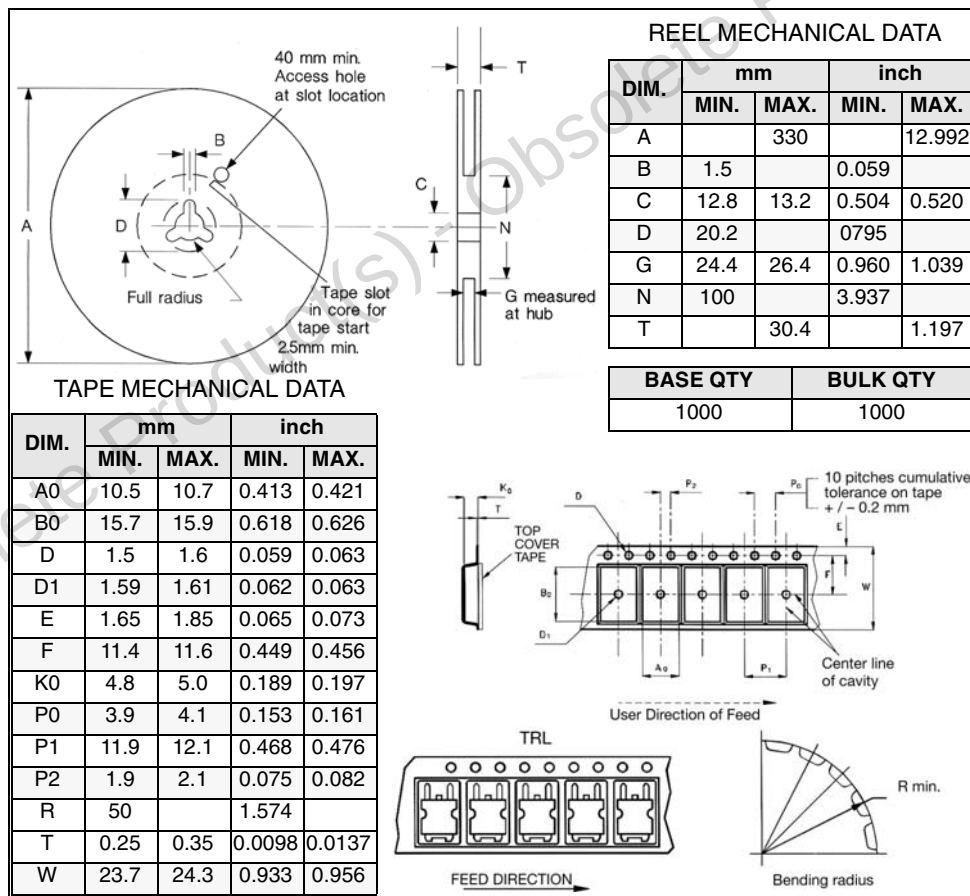


## 5 Packaging mechanical data

### D<sup>2</sup>PAK FOOTPRINT



### TAPE AND REEL SHIPMENT



\* on sales type



## 6 Revision history

**Table 9. Document revision history**

| Date        | Revision | Changes                                                             |
|-------------|----------|---------------------------------------------------------------------|
| 30-Nov-2004 | 1        | First release.                                                      |
| 08-Mar-2005 | 2        | Inserted curves                                                     |
| 22-Mar-2005 | 3        | Modified title                                                      |
| 13-Apr-2005 | 4        | Modified some values                                                |
| 28-Apr-2005 | 5        | Modified some values on <a href="#">Table 8</a>                     |
| 16-May-2005 | 6        | Modified values on <a href="#">Table 7</a>                          |
| 17-Jun-2005 | 7        | Inserted new row on <a href="#">Table 6</a>                         |
| 07-Sep-2005 | 8        | Inserted ecopack indication                                         |
| 05-Oct-2005 | 9        | Modified curves <a href="#">Figure 8</a> , <a href="#">Figure 9</a> |
| 09-Nov-2005 | 10       | Modified <a href="#">Figure 11</a>                                  |
| 14-Nov-2006 | 11       | New template, new value on <a href="#">Absolute maximum ratings</a> |
| 19-Jan-2007 | 12       | Typo mistake on <a href="#">Table 7</a>                             |
| 17-Nov-2008 | 13       | Corrected marking in <a href="#">Table 1</a>                        |

**Please Read Carefully:**

Information in this document is provided solely in connection with ST products. STMicroelectronics NV and its subsidiaries ("ST") reserve the right to make changes, corrections, modifications or improvements, to this document, and the products and services described herein at any time, without notice.

All ST products are sold pursuant to ST's terms and conditions of sale.

Purchasers are solely responsible for the choice, selection and use of the ST products and services described herein, and ST assumes no liability whatsoever relating to the choice, selection or use of the ST products and services described herein.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted under this document. If any part of this document refers to any third party products or services it shall not be deemed a license grant by ST for the use of such third party products or services, or any intellectual property contained therein or considered as a warranty covering the use in any manner whatsoever of such third party products or services or any intellectual property contained therein.

**UNLESS OTHERWISE SET FORTH IN ST'S TERMS AND CONDITIONS OF SALE ST DISCLAIMS ANY EXPRESS OR IMPLIED WARRANTY WITH RESPECT TO THE USE AND/OR SALE OF ST PRODUCTS INCLUDING WITHOUT LIMITATION IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION), OR INFRINGEMENT OF ANY PATENT, COPYRIGHT OR OTHER INTELLECTUAL PROPERTY RIGHT.**

**UNLESS EXPRESSLY APPROVED IN WRITING BY AN AUTHORIZED ST REPRESENTATIVE, ST PRODUCTS ARE NOT RECOMMENDED, AUTHORIZED OR WARRANTED FOR USE IN MILITARY, AIR CRAFT, SPACE, LIFE SAVING, OR LIFE SUSTAINING APPLICATIONS, NOR IN PRODUCTS OR SYSTEMS WHERE FAILURE OR MALFUNCTION MAY RESULT IN PERSONAL INJURY, DEATH, OR SEVERE PROPERTY OR ENVIRONMENTAL DAMAGE. ST PRODUCTS WHICH ARE NOT SPECIFIED AS "AUTOMOTIVE GRADE" MAY ONLY BE USED IN AUTOMOTIVE APPLICATIONS AT USER'S OWN RISK.**

Resale of ST products with provisions different from the statements and/or technical features set forth in this document shall immediately void any warranty granted by ST for the ST product or service described herein and shall not create or extend in any manner whatsoever, any liability of ST.

ST and the ST logo are trademarks or registered trademarks of ST in various countries.

Information in this document supersedes and replaces all information previously supplied.

The ST logo is a registered trademark of STMicroelectronics. All other names are the property of their respective owners.

© 2008 STMicroelectronics - All rights reserved

STMicroelectronics group of companies

Australia - Belgium - Brazil - Canada - China - Czech Republic - Finland - France - Germany - Hong Kong - India - Israel - Italy - Japan - Malaysia - Malta - Morocco - Singapore - Spain - Sweden - Switzerland - United Kingdom - United States of America

[www.st.com](http://www.st.com)